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Re:	Application No.:	09/867,766
	Attorney Docket No.:	60188-075

Message:

This Amendment is being filed in response to the Office Action dated July 1, 2005.

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U.S. practice conducted through McDermott Will & Emery LLP.
600 Thirteenth Street, N.W. Washington, D.C. 20005-3096

Telephone: 202.756.8000

OCT 03 2005

Docket No.: 060188-0075

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Makoto FUJIWARA

Application No.: 09/867,766

Filed: May 31, 2001

Response Under 37 CFR 1.116 - Expedited Procedure

: Customer Number: 20277

: Confirmation Number: 5700

: Group Art Unit: 2134

: Examiner: Poltorak, Piotr

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING SEMICONDUCTOR INTEGRATED CIRCUIT

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.



No additional fee is required.



Applicant is entitled to small entity status under 37 CFR 1.27



Also attached: Request for Acknowledgment of the Cited Art, Information Disclosure Statement, Form PTO 1449, filed May 31, 2001

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	2	20	0	\$50.00 =	\$0.00
Independent Claims	2	3	0	\$200.00 =	\$0.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00



Please charge my Deposit Account No. 500417 in the amount of \$0.00.



The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP


Ramyar M. Farid

Registration No. 46,692

Please recognize our Customer No. 20277 as our
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Date: October 3, 2005

OCT 03 2005

Docket No.: 060188-0075

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Makoto FUJIWARA

RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE

Application No.: 09/867,766

Customer No.: 20277
Confirmation No.: 5700

Filed: May 31, 2001

Group Art Unit: 2134

Examiner: Poltorak, Piotr

Title: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING
SEMICONDUCTOR INTEGRATED CIRCUIT

AMENDMENT UNDER 37 CFR 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 1, 2005, having a three-month shortened statutory period for response set to expire on October 1, 2005 (Saturday), reconsideration of the above-identified application is respectfully requested in view of the following amendment and remarks.